ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

EFS ID:

SYSTEM AND METHOD TO TEST INTEGRATED CIRCUITS ON A **WAFER**

Submision Type: **Utility Patent Filing**

Application Number:

10/707205

51345

Server Response:

Confirmation Code	Message	
	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application	
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First Named Applicant: Lawrence Pileggi Attorney Docket Number: 361007.000035

Timestamp: 2003-11-26 14:08:45 EDT

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File Listing:

Doc. Name	Eile Name	Cina (Putan)	Data
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us-request	SRC35-usrequ.xml	3805	2003-11-26
us-request	us-request.dtd		2003-11-26
us-request	us-request.xsl		2003-11-26
us-fee-sheet	SRC35-usfees.xml	1717	2003-11-26
us-fee-sheet	us-fee-sheet.xsl	24912	2003-11-26
us-fee-sheet	us-fee-sheet.dtd	11069	2003-11-26
us-declaration	DECPg3.tif	32558	2003-11-26
application-body	Document2-trans.xml	59547	2003-11-26
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application-body	wipo.ent	4956	2003-11-26
application-body	mathml2.dtd	54588	2003-11-26
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application-body	isoamsc.ent	1460	2003-11-26
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application-body	DrwgPg6.tif		16058	2003-11-26
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package-data	us-package-data.xsl		19263	2003-11-26
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